



# Device Simulation for Carbon Nanotube Electronics

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1. Introduction
2. NEGF formalism
3. Simulation Approach
4. Device Analysis
5. Summary

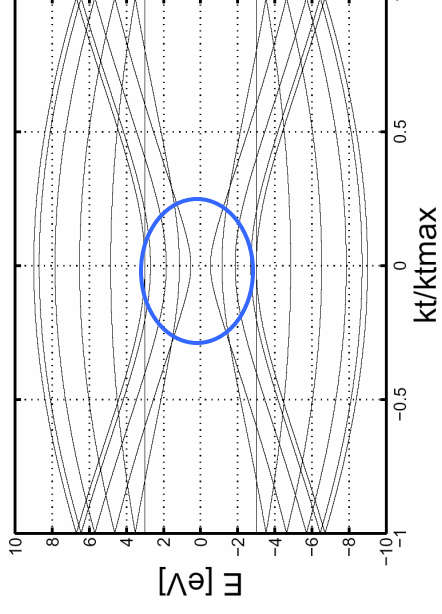
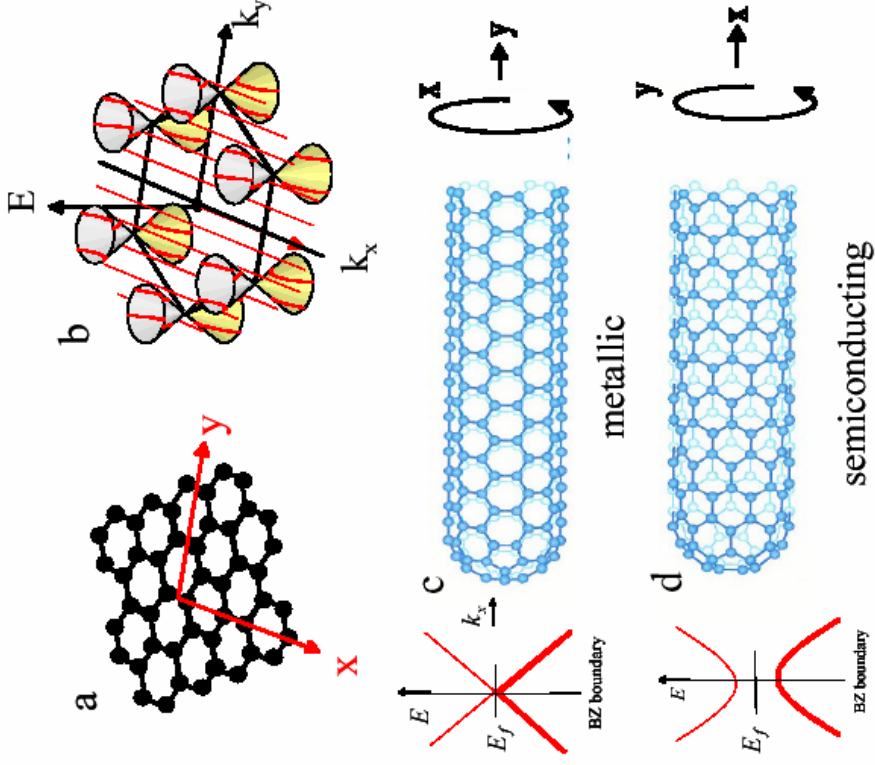
# *Acknowledgements*

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Theory: Mark Lundstrom, Supriyo Datta (Purdue)

Experiment: Hongjie Dai, Ali Javey (Stanford)

# Carbon nanotubes



$$E(k) = \pm \left( \frac{E_G}{2} \right) \sqrt{1 + (3kd/2)^2}$$

$$E_G \approx 0.8eV/d(\text{nm})$$

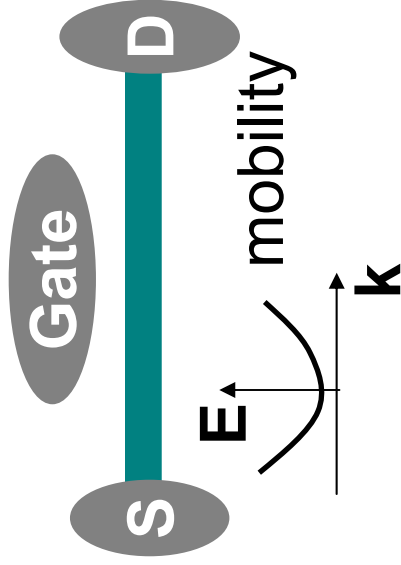
McEuen, Fuhrer, Park, *IEEE Trans. Nanotech.*, **1**, 78, 2002.

(see also: R. Saito, G. Dresselhaus, and M.S. Dresselhaus, *Physical Properties of Carbon Nanotubes*, Imperial College Press, London, 1998.)

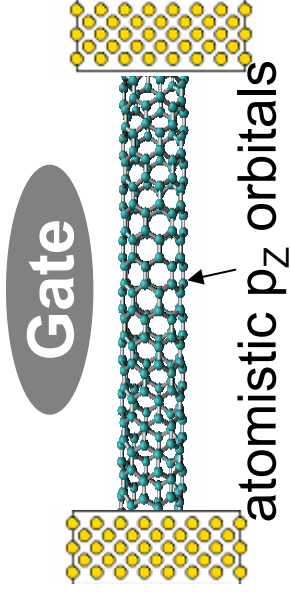
# Top-down and bottom-up view

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## Top-down view



## Bottom-up view



## Quantum approach

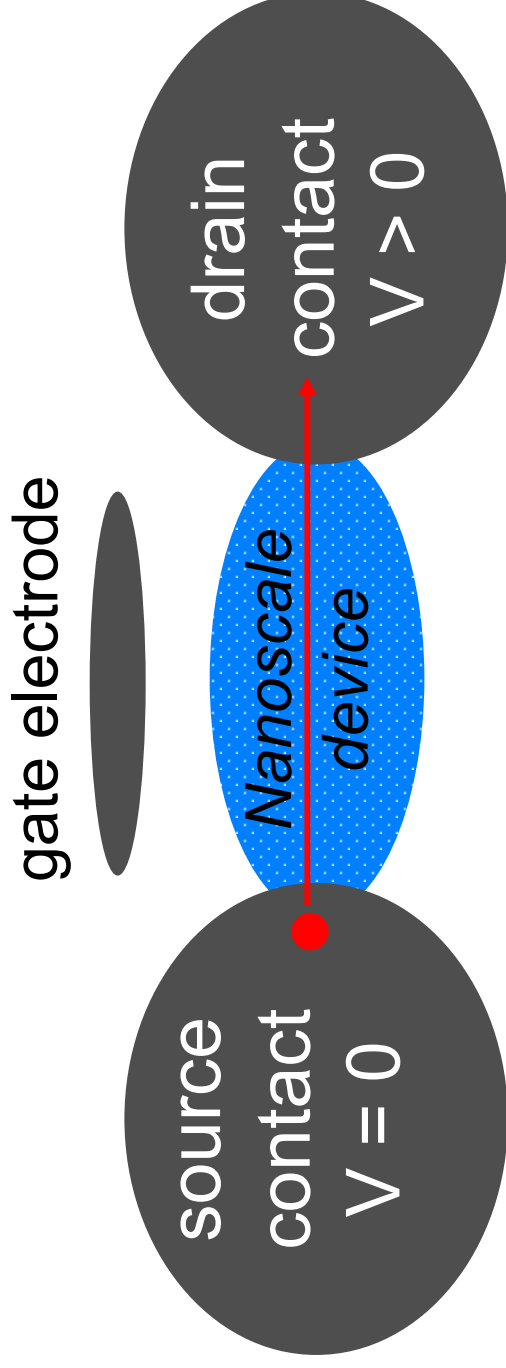
- tunneling at M/CNT contacts
- tunneling and interference in the CNT

## Semiclassical approach

applicable only when quantum effects not important

# Quantum simulation for Nanoelectronics

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Challenges in nanoscale device simulation:

- 1) description at an atomistic level
- 2) quantum description of open systems under bias
- 3) treatment of inelastic scattering

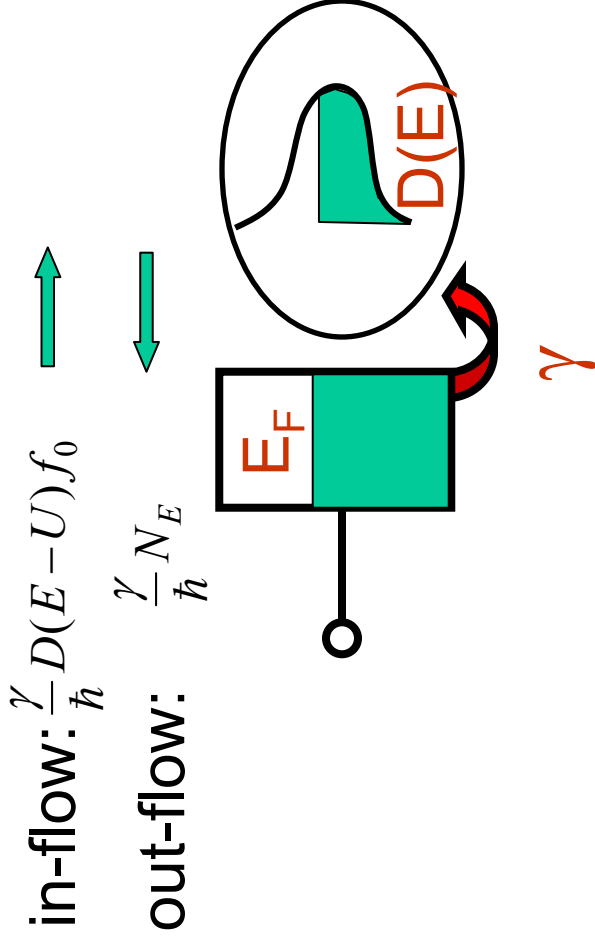
**Our approach: the Green's function formalism**

# Outline

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# One contact



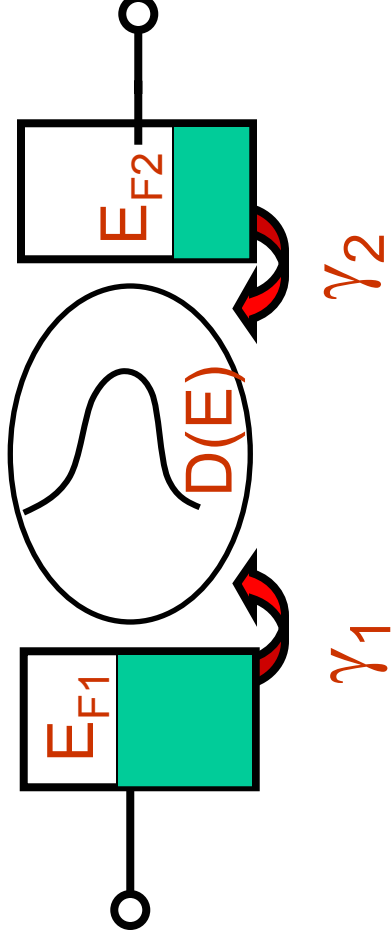
$$\frac{dN_E}{dt} = \frac{\gamma}{\hbar} [D(E-U) f_0 (E - E_F) - N_E]$$

$$N = \int dE D(E-U) f_0 (E - E_F)$$

# Two contacts

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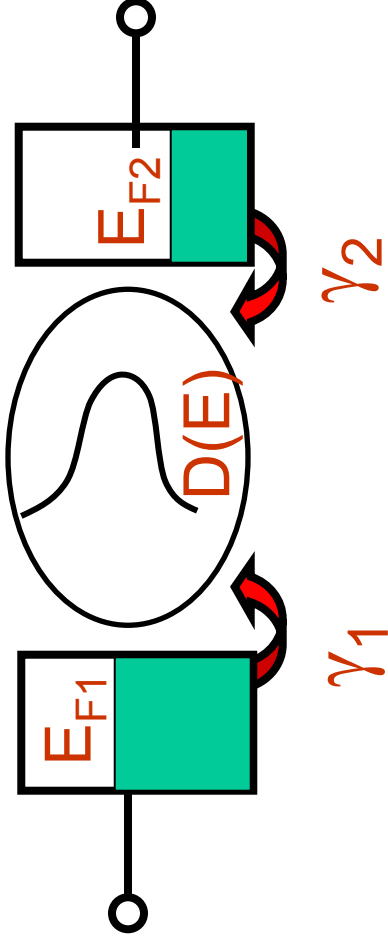
in-flow:  $\frac{\gamma_1}{\hbar} D(E-U) f_1$   $\longleftrightarrow$  in-flow:  $\frac{\gamma_2}{\hbar} D(E-U) f_2$   
 out-flow:  $\frac{\gamma_1}{\hbar} N_E$   $\longleftrightarrow$  out-flow:  $\frac{\gamma_2}{\hbar} N_E$



$$\frac{dN_E}{dt} = \frac{\gamma_1}{\hbar} [D(E) f_1 - N_E] + \frac{\gamma_2}{\hbar} [D(E) f_2 - N_E]$$



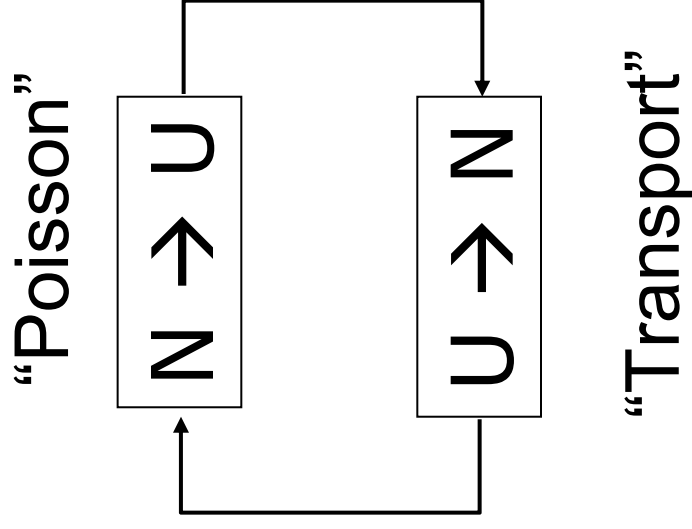
# Two contacts



$$N = \int dE D(E - U) \left[ \frac{\gamma_1 f_1 + \gamma_2 f_2}{\gamma_1 + \gamma_2} \right]$$

$$I = \frac{2q}{\hbar} \int dE D(E - U) \frac{\gamma_1 \gamma_2}{\gamma_1 + \gamma_2} [f_1 - f_2]$$

$$U = U_L + U_0(N - N_0)$$



# Multiple levels

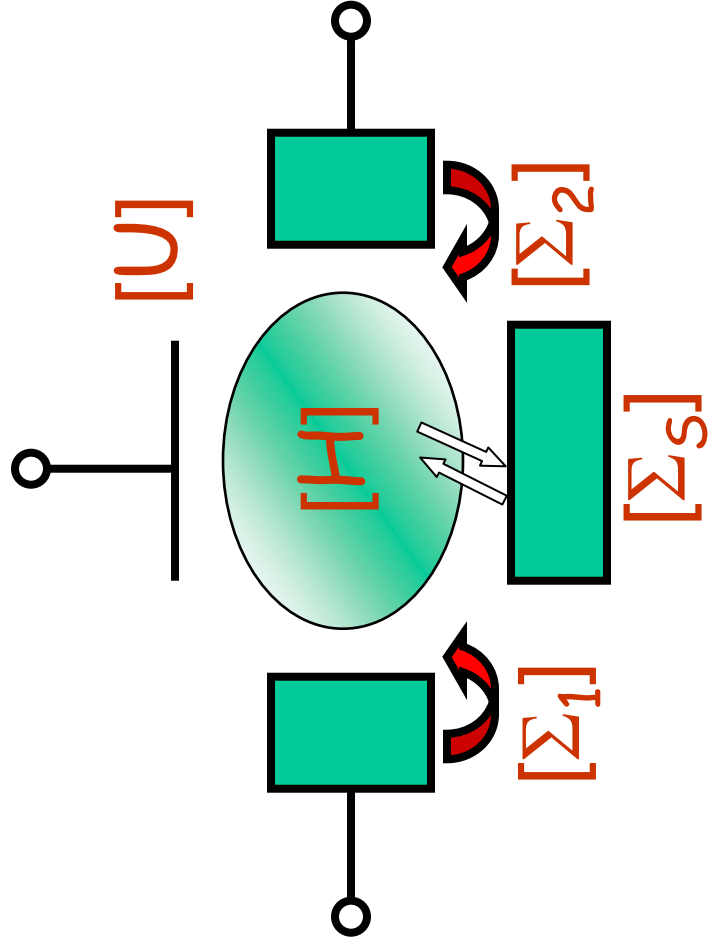
$\varepsilon \rightarrow [H]$

$\gamma \rightarrow [\Gamma]$

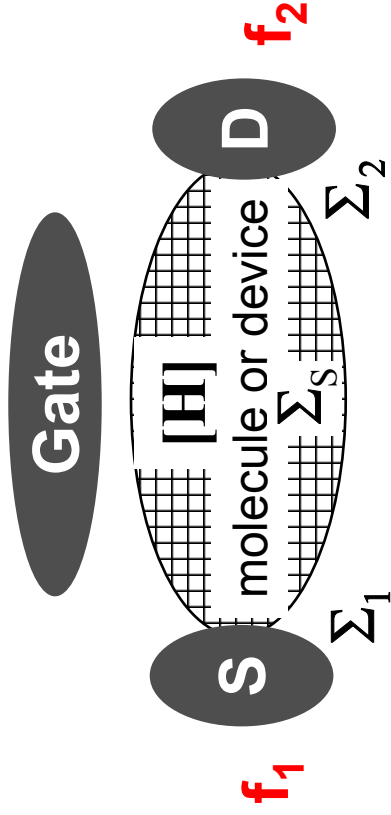
$[\Sigma]$

$N \rightarrow [\rho]$

$$G = [ES - H - \Sigma]^{-1}$$



# Non equilibrium Green's Function (NEGF)



$$G = [EI - H - \underbrace{\Sigma_1 - \Sigma_2 - \Sigma_S}_{\text{device contacts scattering}}]^{-1}$$

device contacts scattering

**Charge density (ballistic)**

$$[\rho] = \int [A_1(E)f_1(E) + A_2(E)f_2(E)] \frac{dE}{2\pi}$$

**Current**

$$I_D = \frac{2q}{h} \int T(E) (f_1(E) - f_2(E)) dE$$

$$A_{1,2}(E) = G \Gamma_{1,2} G^+$$

$$T(E) = \text{Trace}[\Gamma_1 G \Gamma_2 G^+]$$

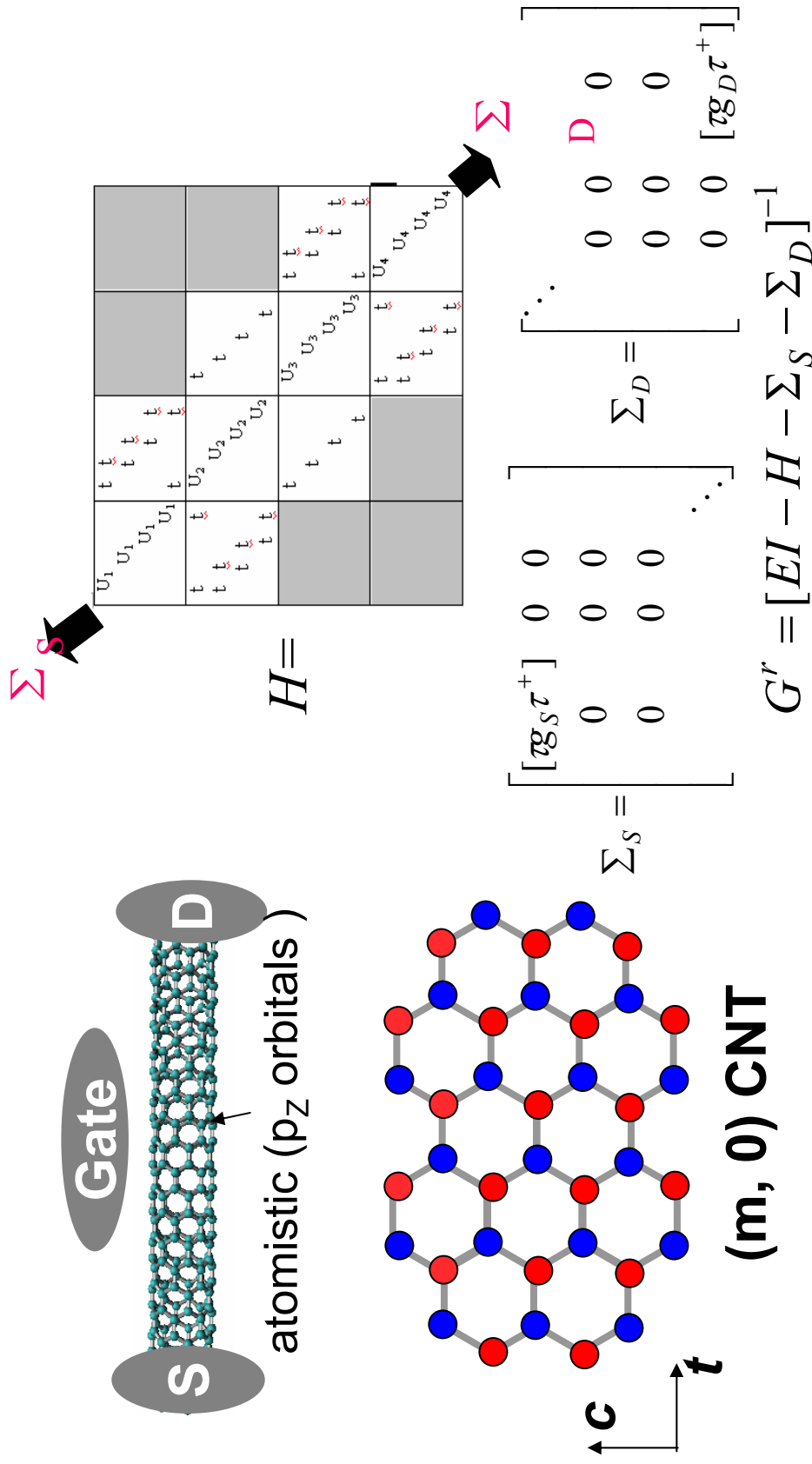
$$\Gamma_{1,2} = i[\Sigma_{1,2} - \Sigma_{1,2}^+]$$

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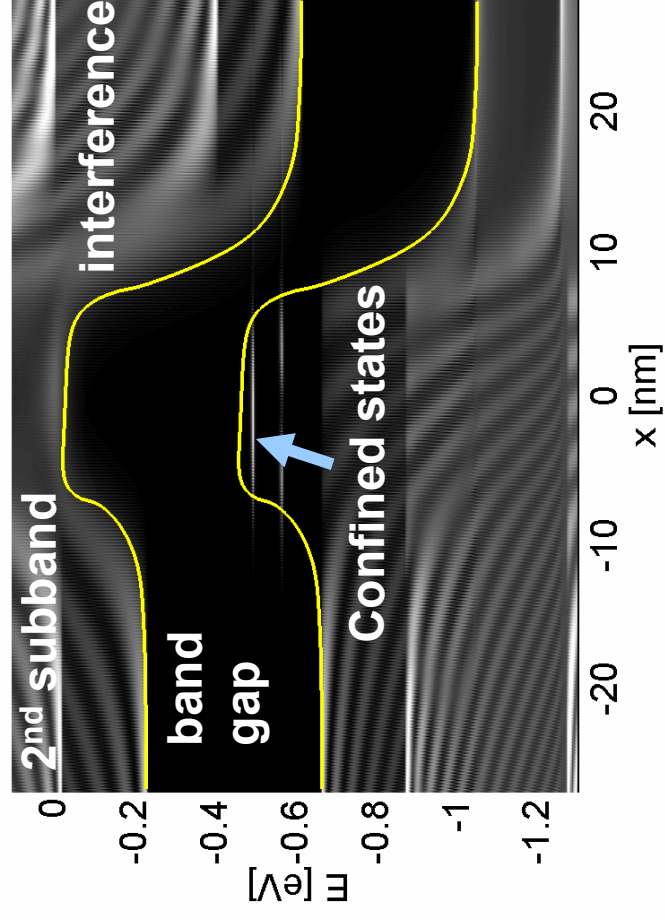
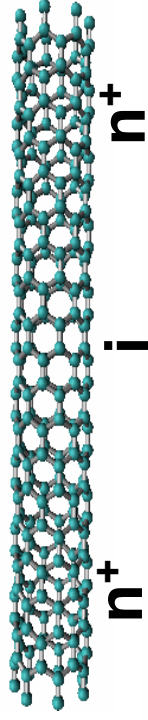
# Real-space basis (ballistic)



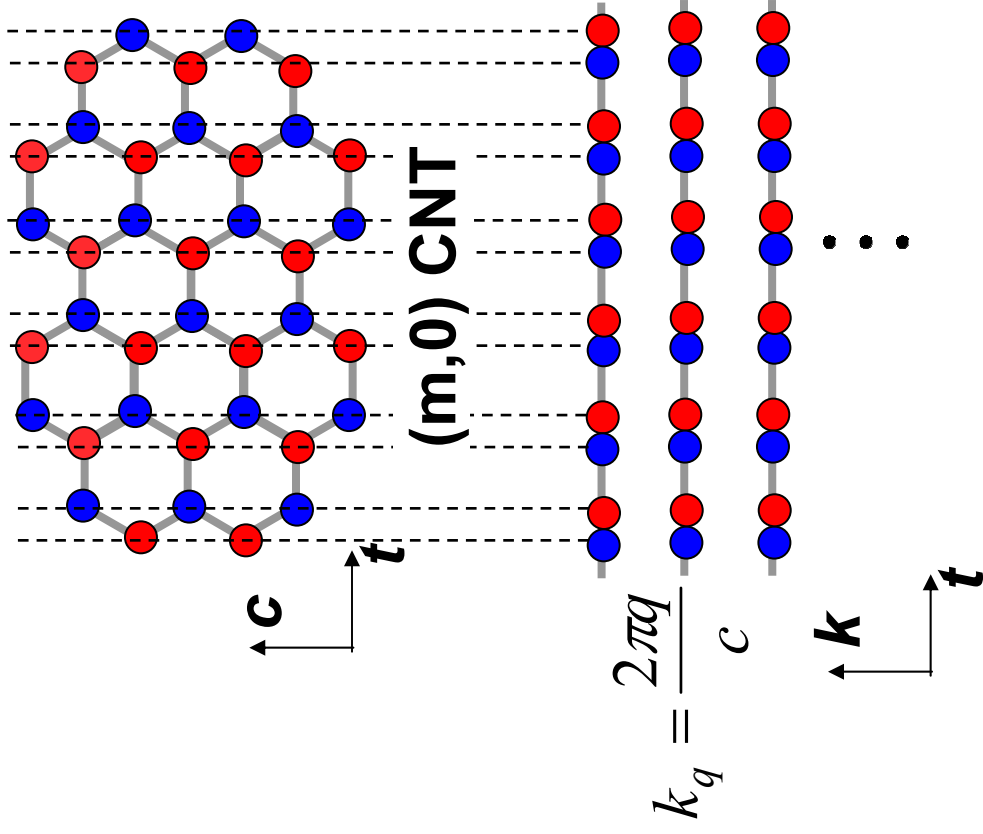
Recursive algorithm for  $G^r$ :  $O(m^3N)$

# Real-space results

Gate



# Mode-space approach (ballistic)

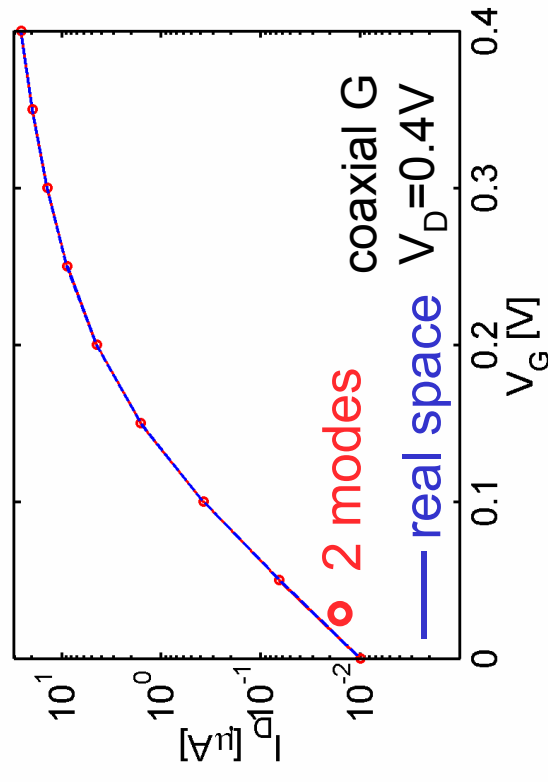
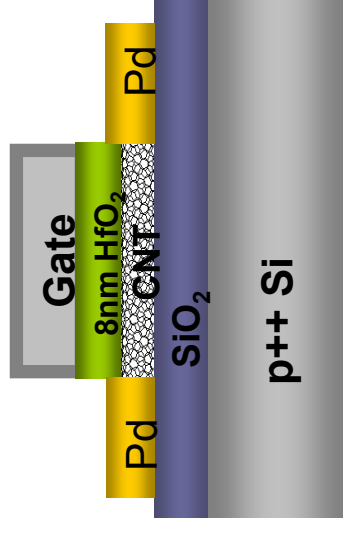
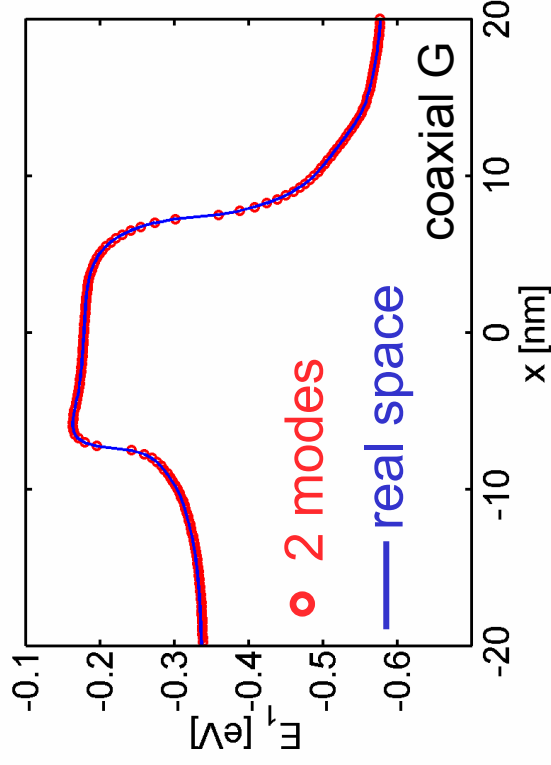
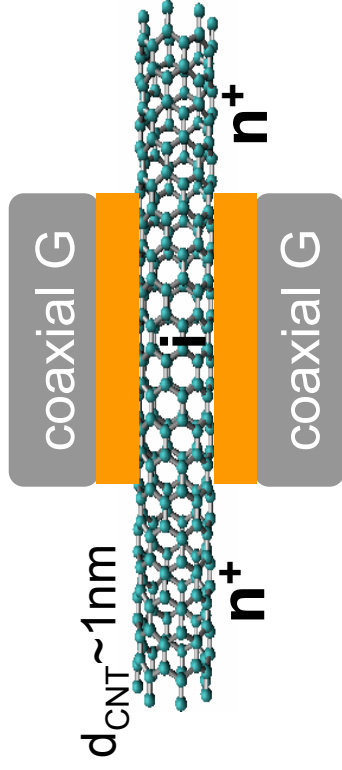


The  $q$ th mode

$$H_q = \begin{bmatrix} u_1 & b_q & & & & \\ b_q & u_2 & t & & & \\ & t & u_3 & \dots & & \\ & & \dots & \dots & \dots & \\ & & & & b_q & u_N \end{bmatrix}$$

- $\Sigma_S(1,1)$  and  $\Sigma_D(N,N)$  analytically computed
- Computational cost:  $O(N)$   
real space  $O(m^3N)$

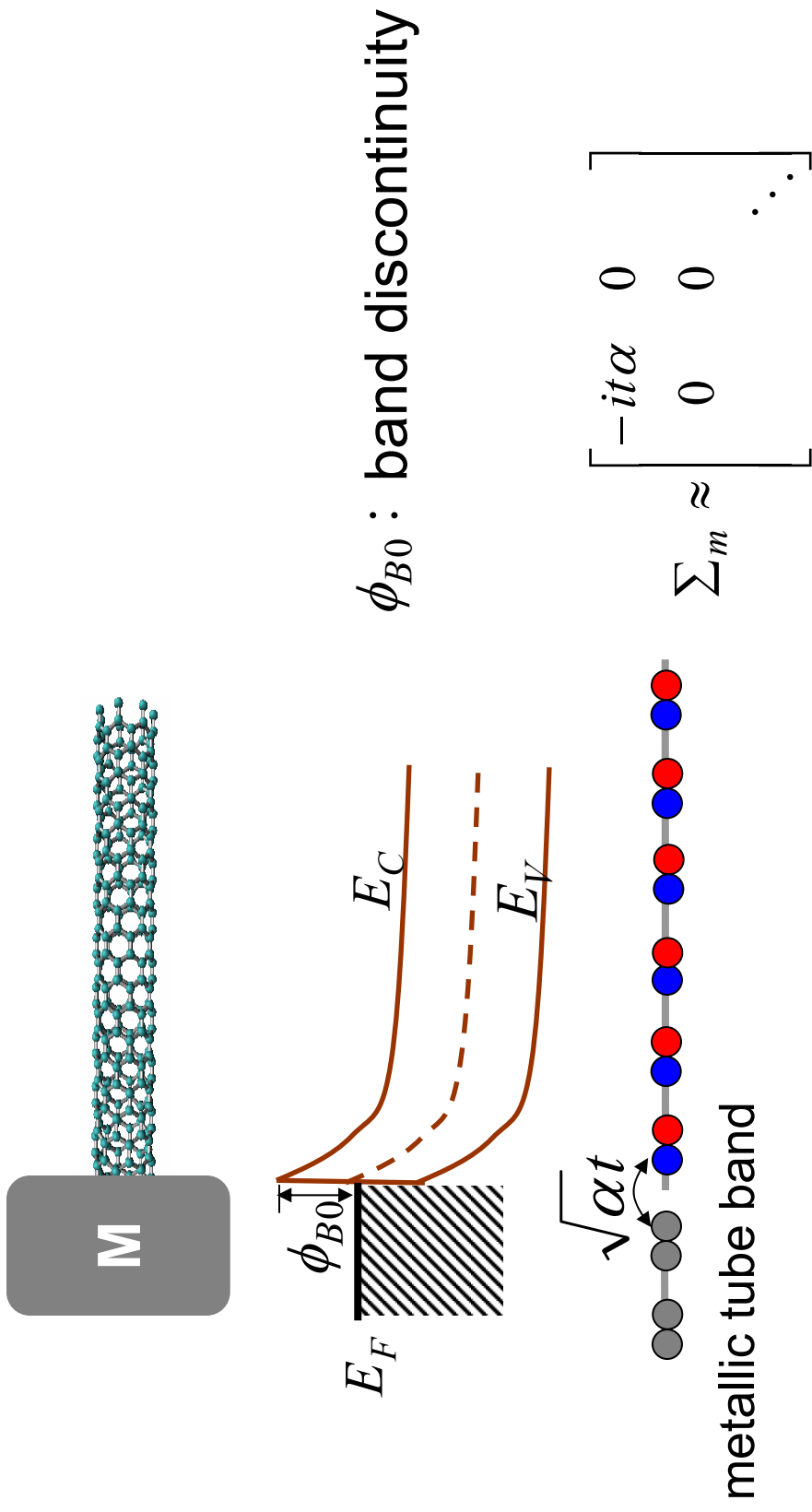
# Mode-space results



Conduction band profile  
(ON)

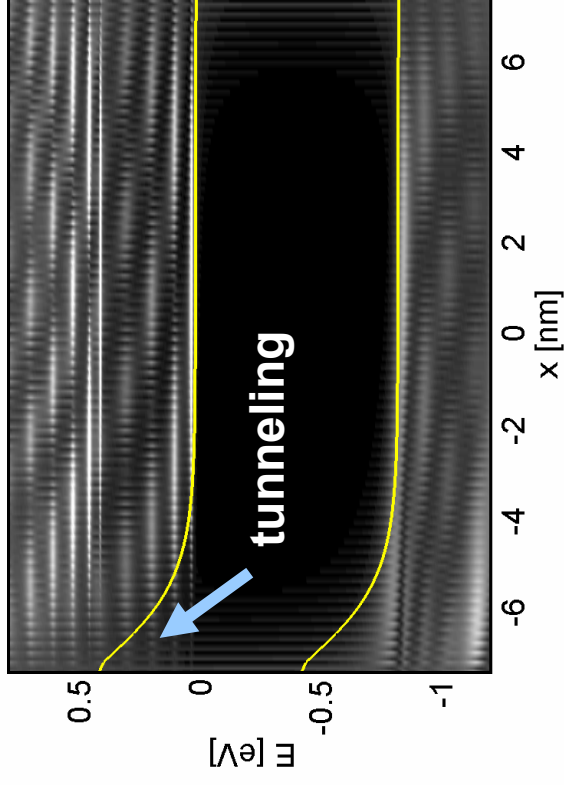
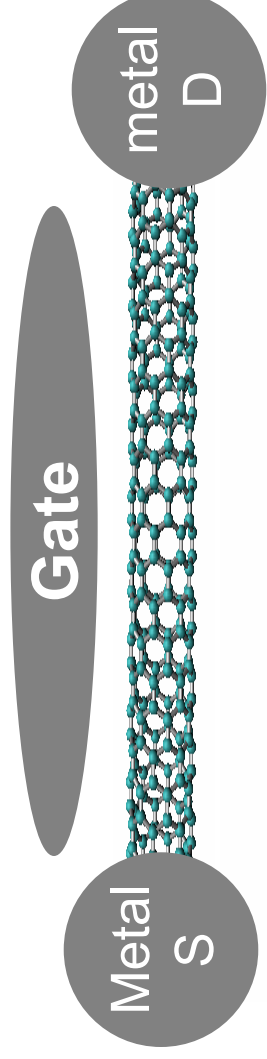


# Treatment of M/CNT contacts



# Treatment of M/CNT contacts

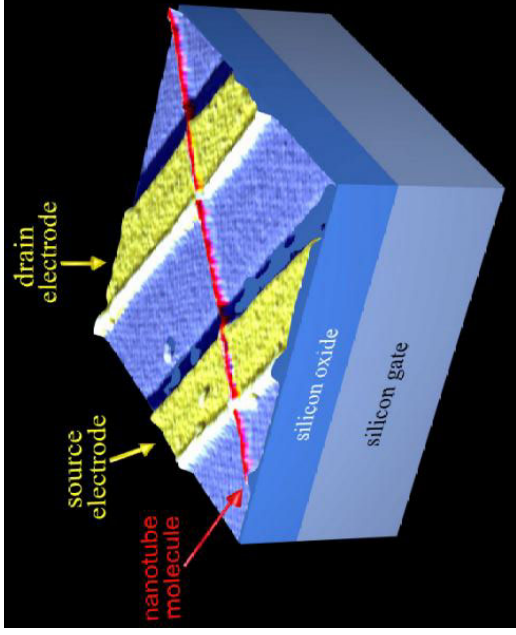
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$$V_D = V_G = 0.4V$$

Charge transfer in unit cell: Leonard et al., APL, **81**, 4835, 2002

# 3D Poisson solver



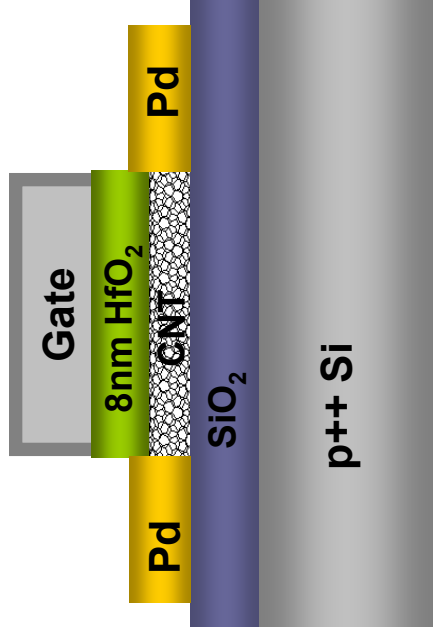
**Method of moments:**

$$V(\vec{r}) = \int K(\vec{r} - \vec{r}') \rho(\vec{r}') d\vec{r}'$$

**Electrostatic kernel:**

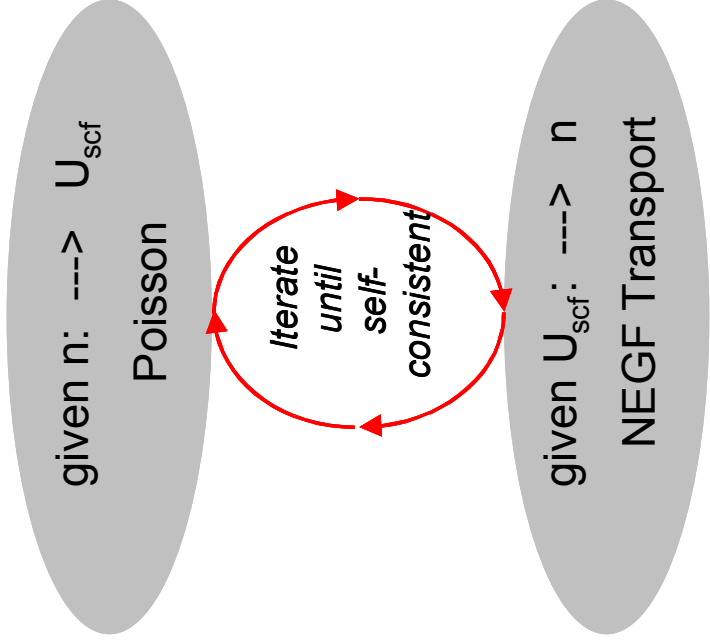
$$K(\vec{r} - \vec{r}')$$

$K(\vec{r} - \vec{r}')$  for 2 types of dielectrics available in Jackson, *Classical Electrodynamics*, 1962



# Numerical techniques

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- **Non-linear Poisson**
- **Recursive algorithm for**  
$$G(E) = [EI - H - \Sigma_S - \Sigma_D]^{-1}$$
- **Gaussian quadrature for doing integral**
- **Parallel different bias points**
- **~20min for full I-V of a 50-nm CNTFET**

# Outline

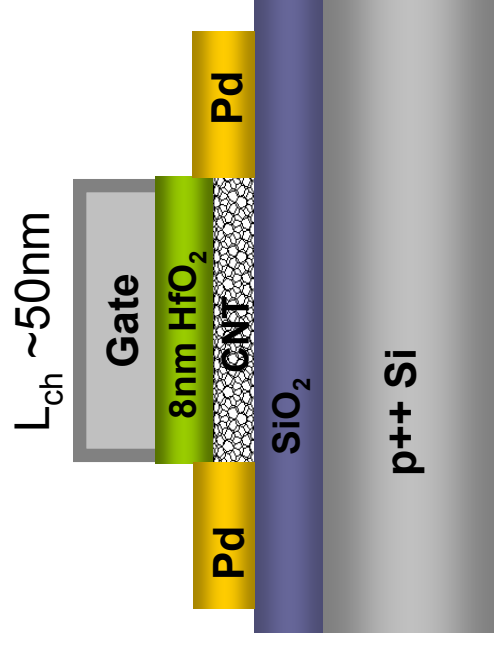
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# Device issues

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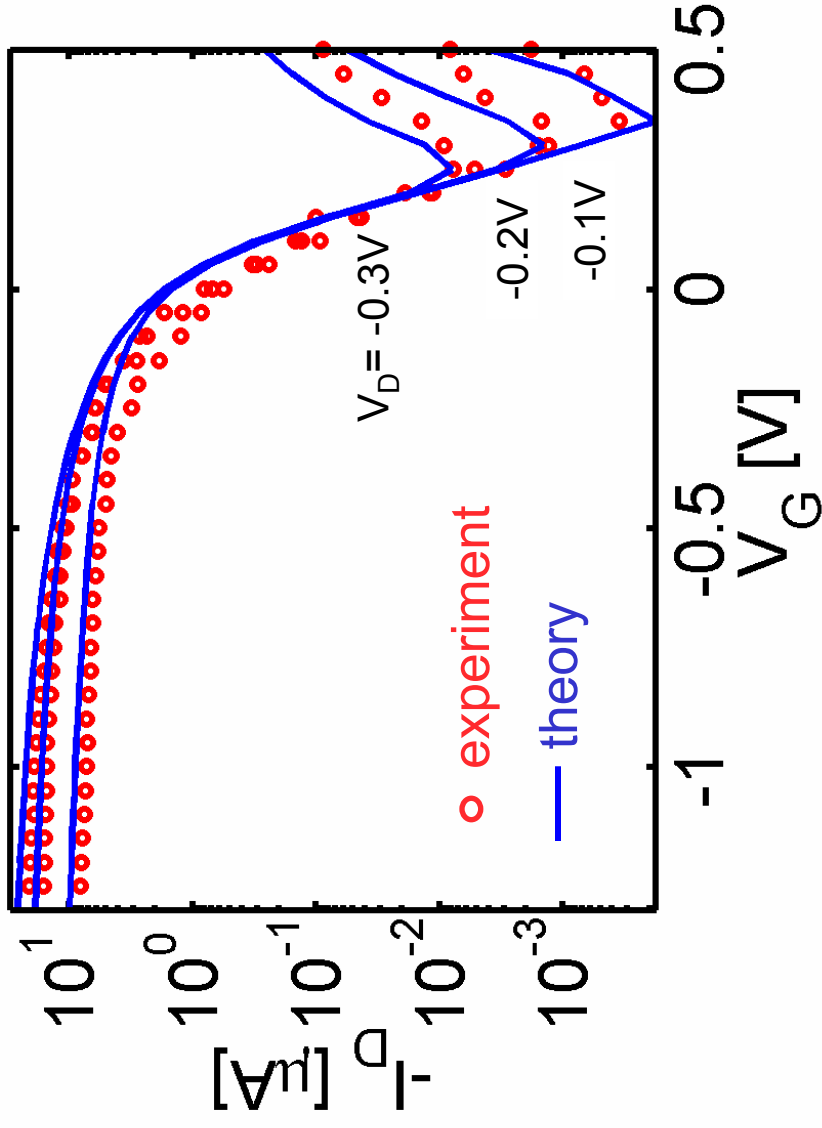
nanotube diameter  $\sim 1.7$  nm



- 1) Can we model and understand I-V?
- 2) How close to the ballistic limit?
- 3) What is the role of scattering?
- 4) How to optimize  $I_{ON}$  ?
- 5) How to reduce  $I_{off}$  ?
- 6) How to compare to Si MOSFETs?

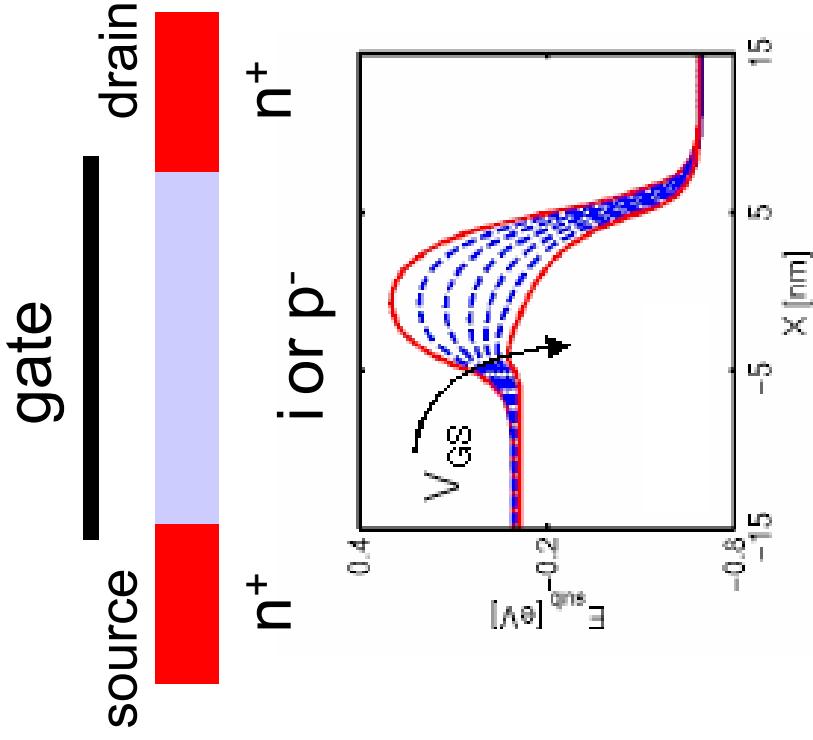
Javey et al, Nano Lett., 2004

# Modeling $I_D$ - $V_G$

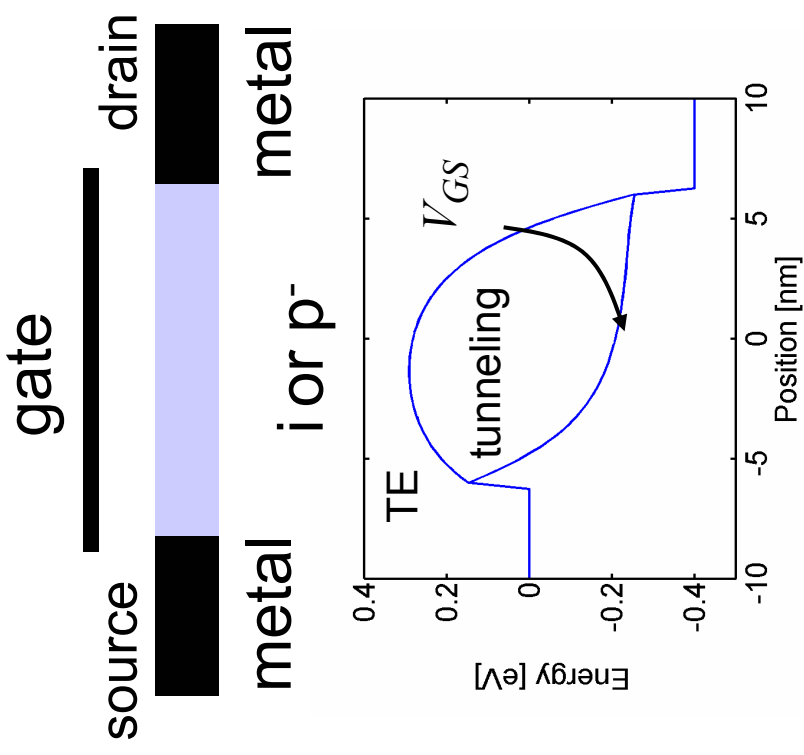


SB height:  $\phi_{\text{Bp}}=0$ ,  $d_{\text{CNT}} \sim 1.7\text{nm}$   $R_S=R_D \sim 1.7\text{K}\Omega$

# Two kinds of transistors



MOSFET



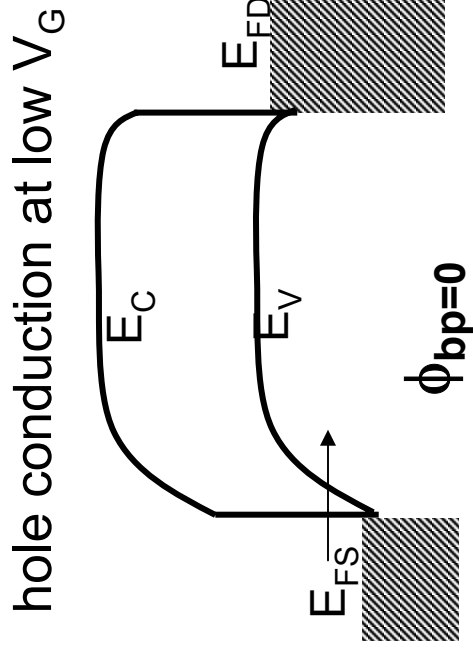
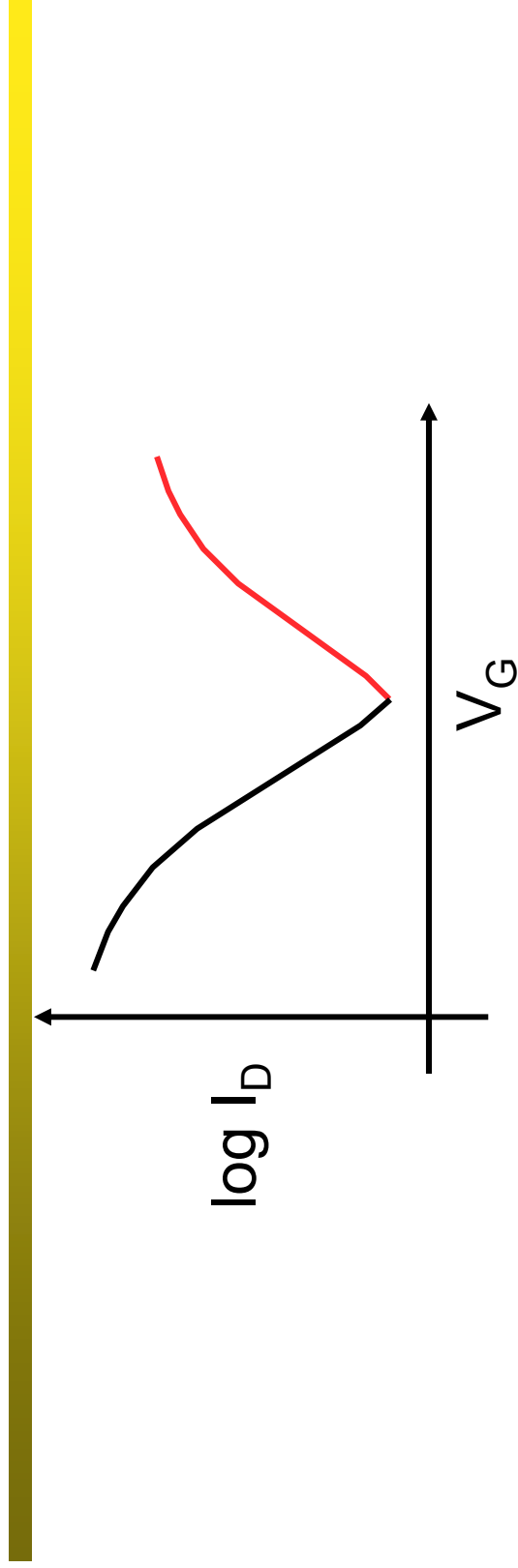
SBFET

Carbon nanotubes as Schottky barrier transistors

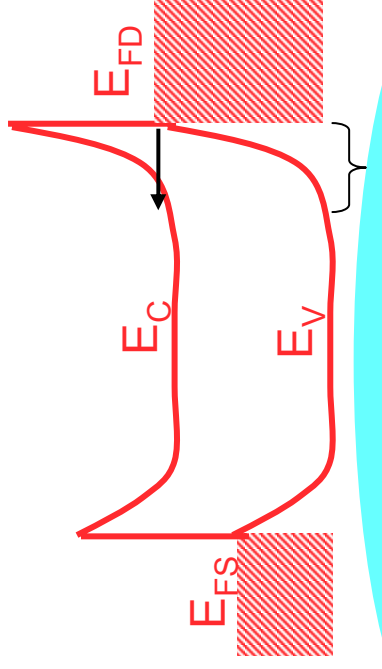
Heinze et al, *PRL*, **89**, 106801, 2002



# Ambipolar conduction (thin oxide)



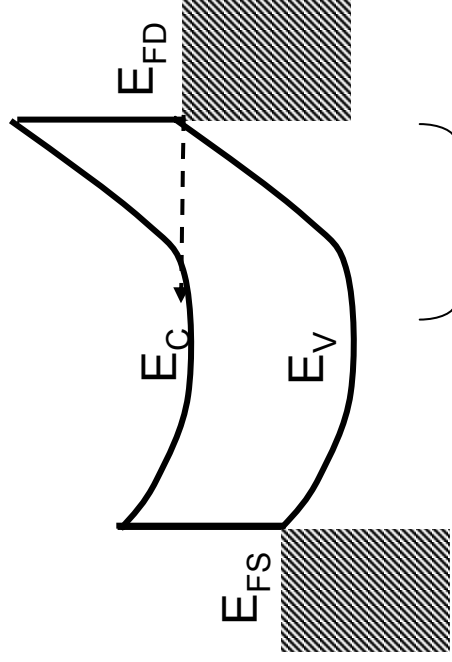
electron conduction at high  $V_G$



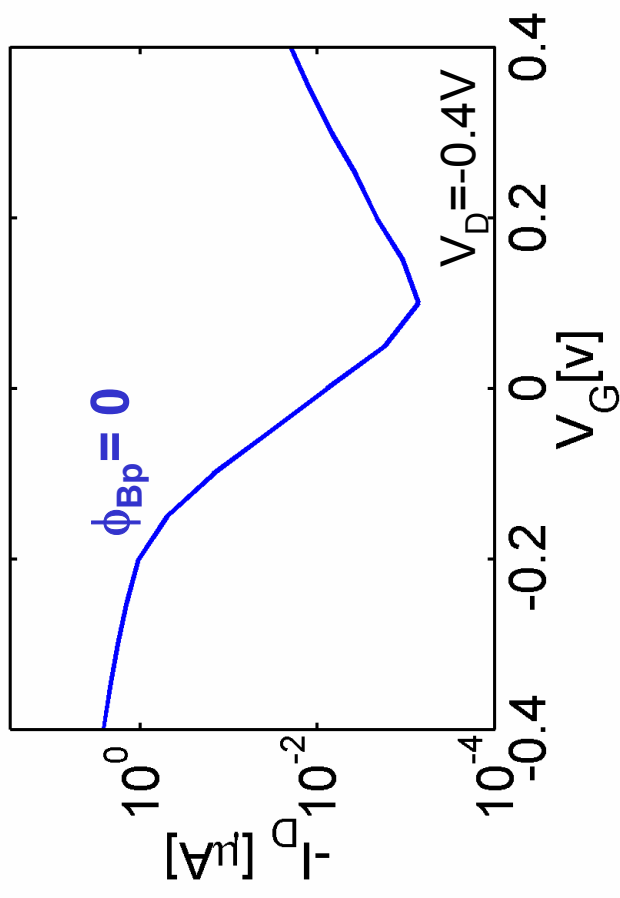
barrier thickness set by  $t_{ins}$   
(geometric screening)

# Thick oxide

opaque barrier for  
electron tunneling

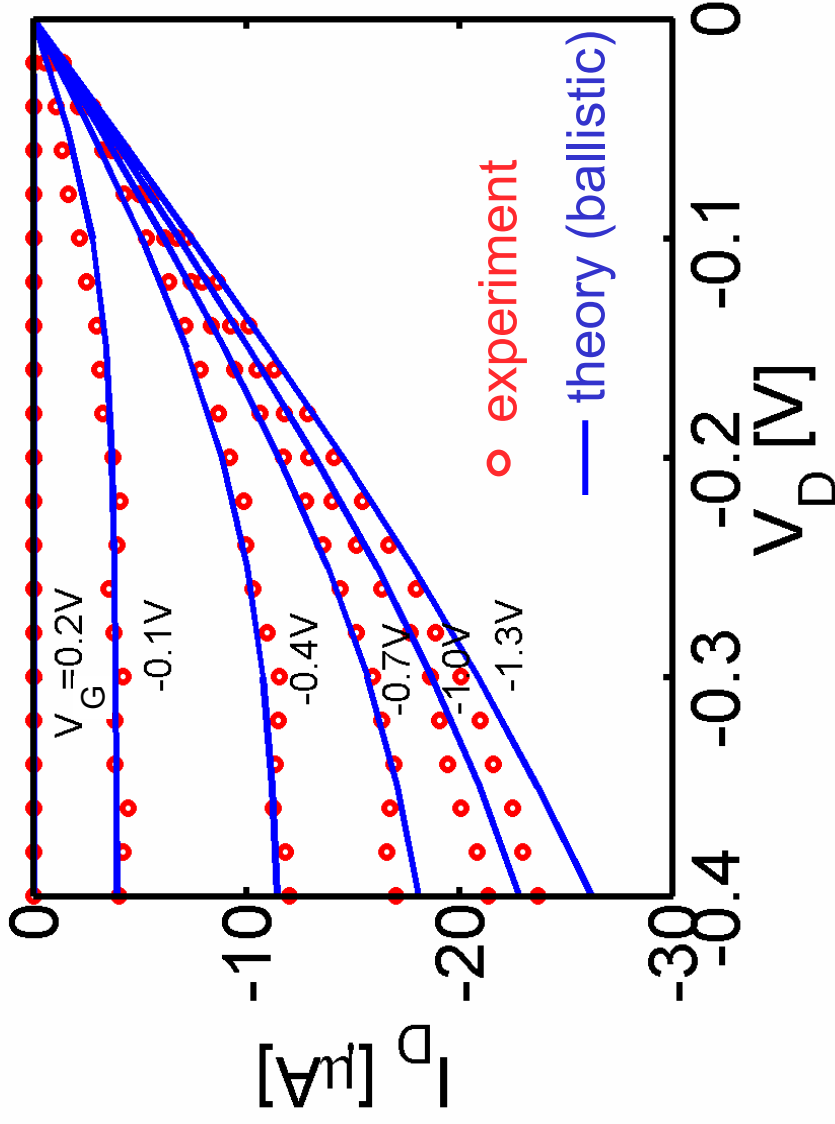


$t_{\text{ox}} = 40 \text{ nm}$



barrier thickness set by  $t_{\text{ins}}$   
(geometric screening)

# How close to ballistic limit?

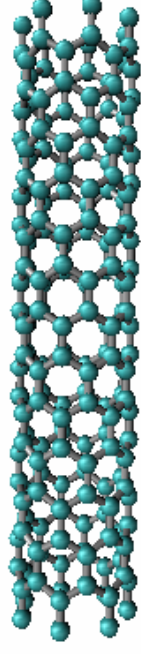
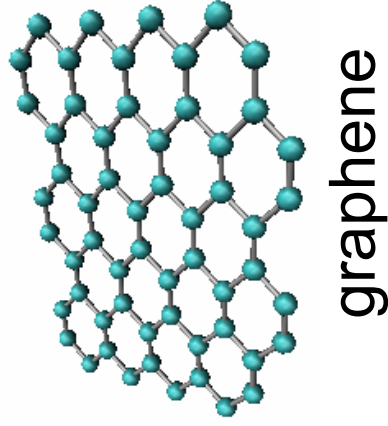


SB height:  $\phi_{\text{Bp}}=0$ ,  $d_{\text{CNT}}\sim 1.7\text{nm}$ ,  $R_S=R_D\sim 1.7\text{K}\Omega$

⇒ Deliver near-ballistic DC on-current

# No surface roughness scattering in CNTs

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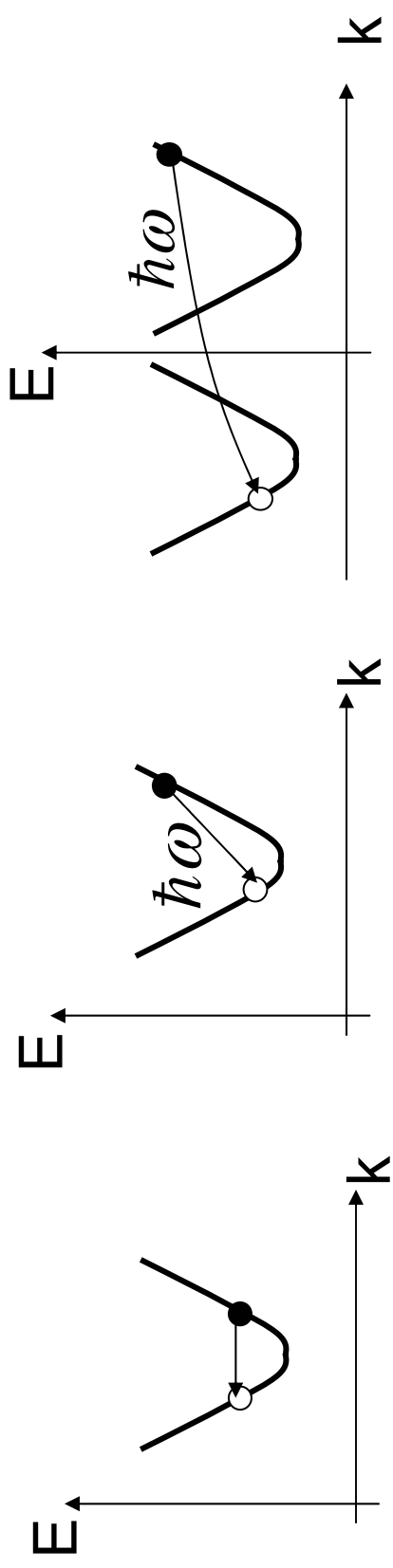


CNT

## Phonon scattering dominates in CNTs

Yao, Kane, and Dekker, *Phys. Rev. Lett.*, **84**, 2941, 2000

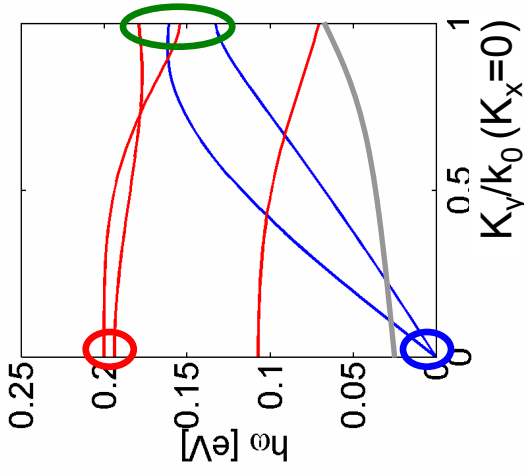
# Phonon scattering in CNTs



● AP

● OP (intra.)

● OP (intervalley)



AP: long mfp ( $\lambda_1^{high} \sim 1 \mu\text{m}$ )

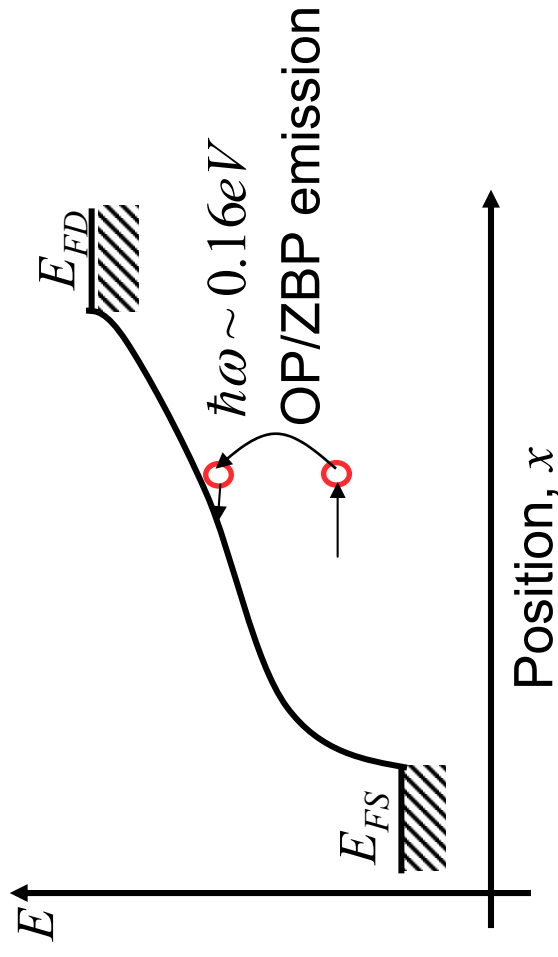
OP: short mfp ( $\lambda_2^{high} \sim 10 \text{nm}$ )

Javey, Guo, Paulsson et al., *Phys. Rev. Lett.*, **92**, 106804, 2004

Park, Rosenblatt, Yaish et al., *Nano Lett.*, **4**, 517

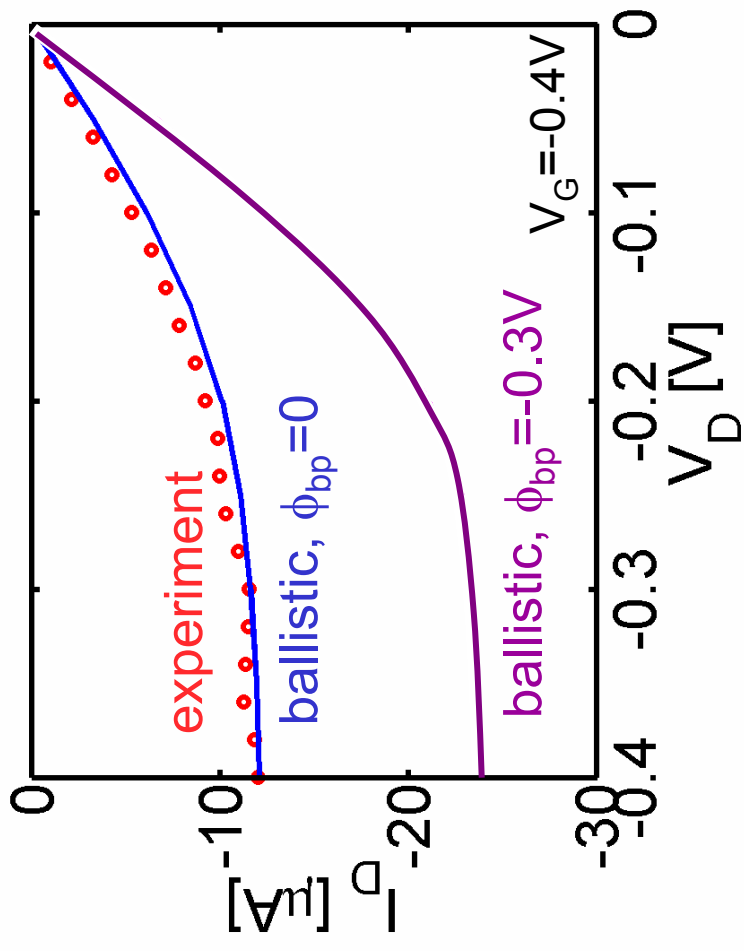
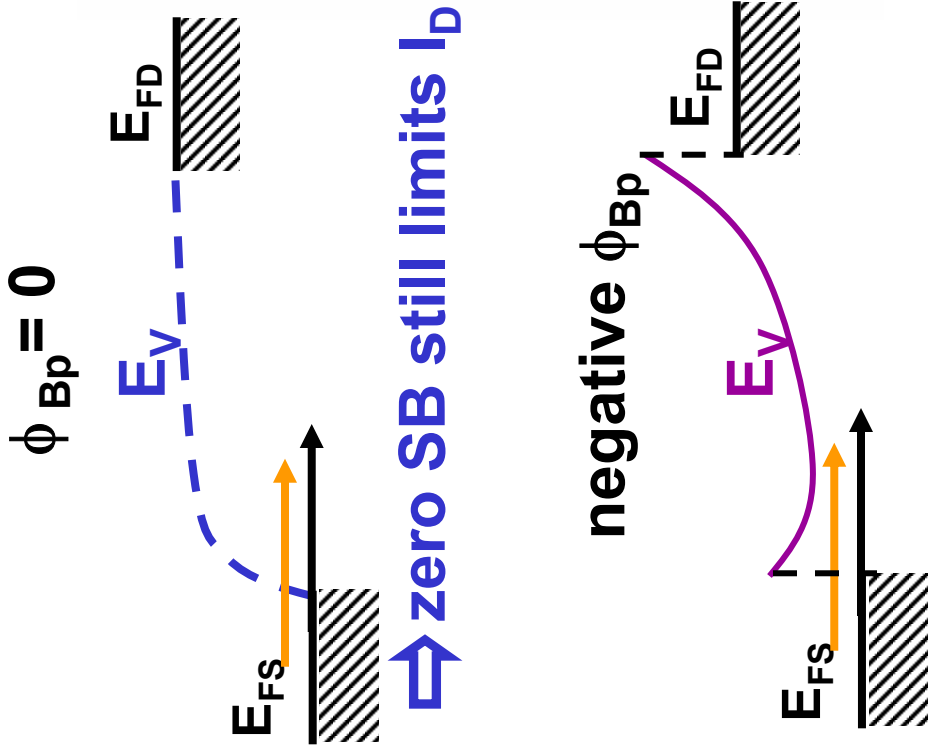
# Small effect of OP scattering

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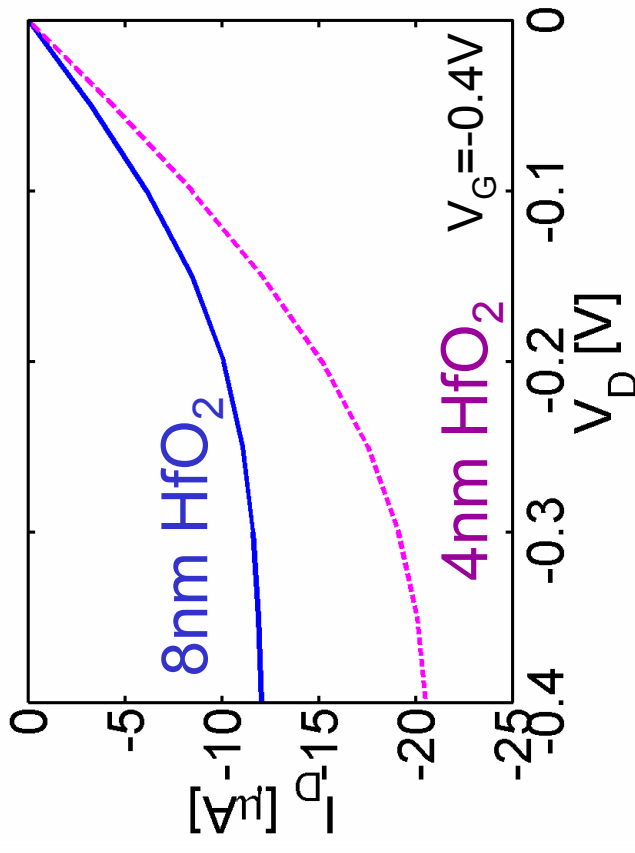
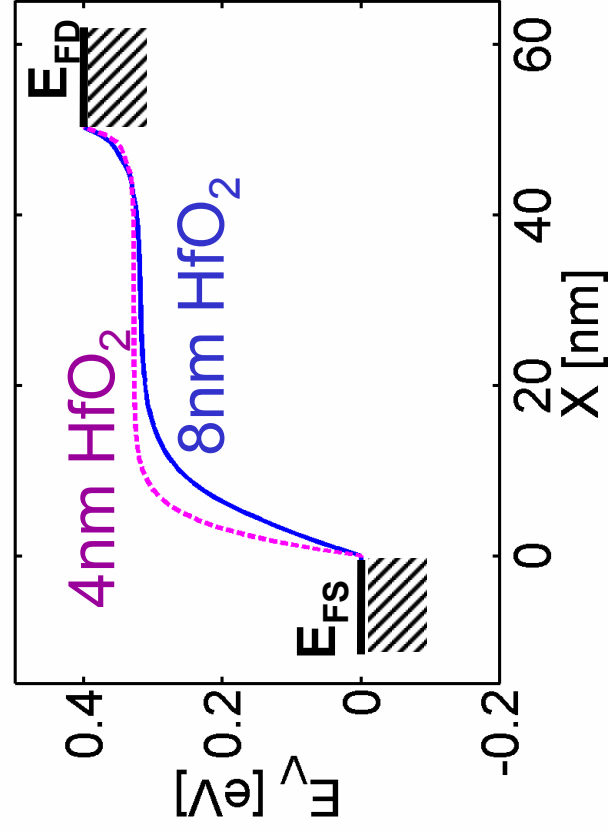
⇒ Deliver near-ballistic DC on-current  
confirmed by a separate Monte-Carlo simulation

# How close to the ballistic limit?



Guo and Lundstrom,  
*IEEE TED*, **49**, 1897, 2002 (silicon)

# Improving $I_{ON}$ : Scaling $t_{ins}$

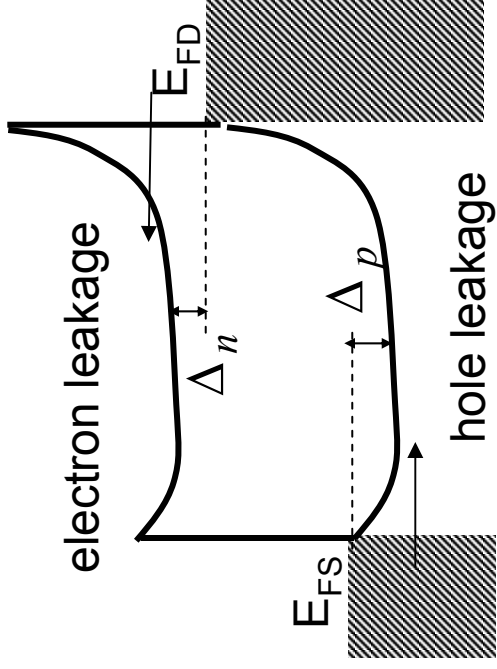


barrier thickness set by  $t_{ins}$   
(geometric screening)



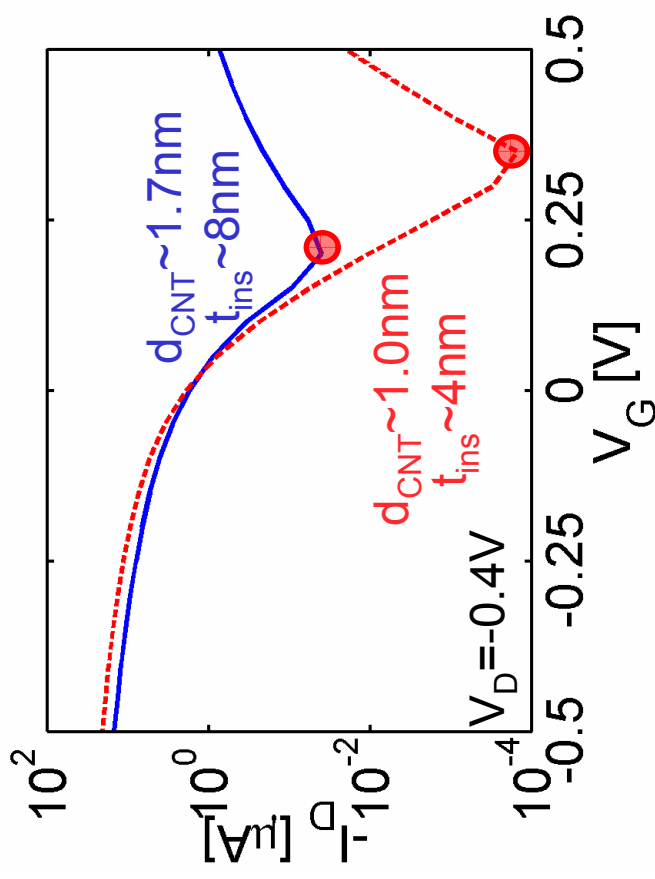
# Reduce $I_{\text{off}}$ for thin $t_{\text{ins}}$

nearly transparent



$$\Delta_n \sim \Delta_p \sim \frac{E_g - eV_D}{2}$$

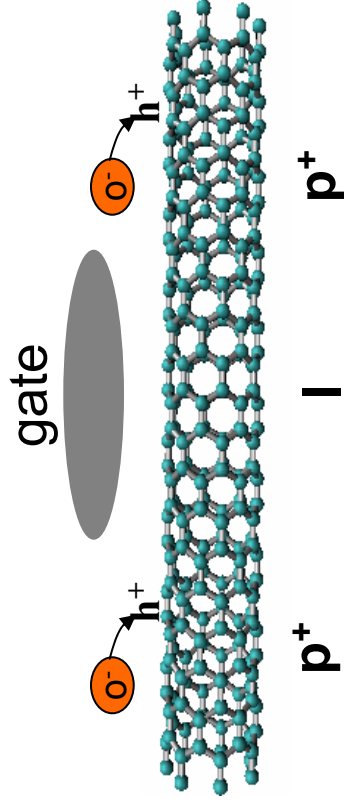
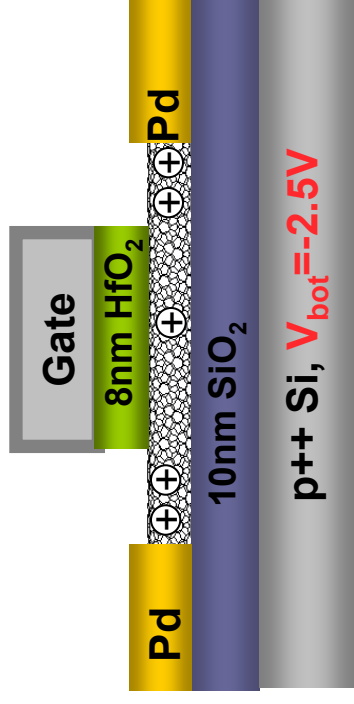
$$E_g \sim 0.8 \text{ eV} / d(\text{nm})$$



**small  $d_{\text{CNT}}$  reduces  $I_{\text{min}}$**

# Reduce $I_{\text{off}}$ using MOSFET-like structure

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## chemical S/D doping

Chen et al., IEDM Tech Dig, 2004

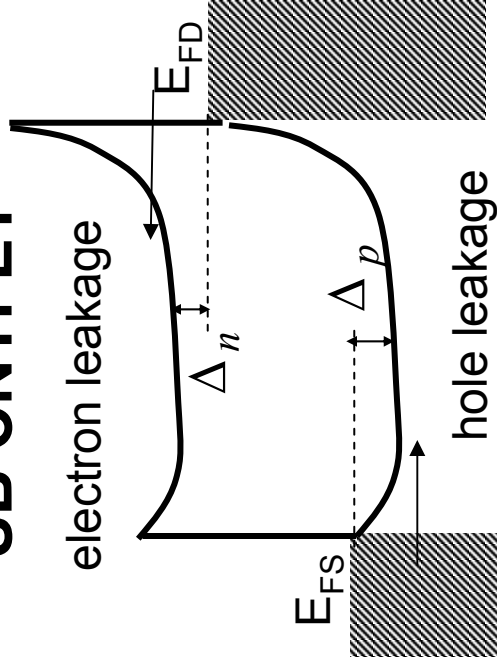
Javey et al., Nano Lett., 2005

## electrical S/D doping

Appenzeller et al, PRL, 2004

# Reduce $I_{\text{off}}$ using MOSFET-like structure

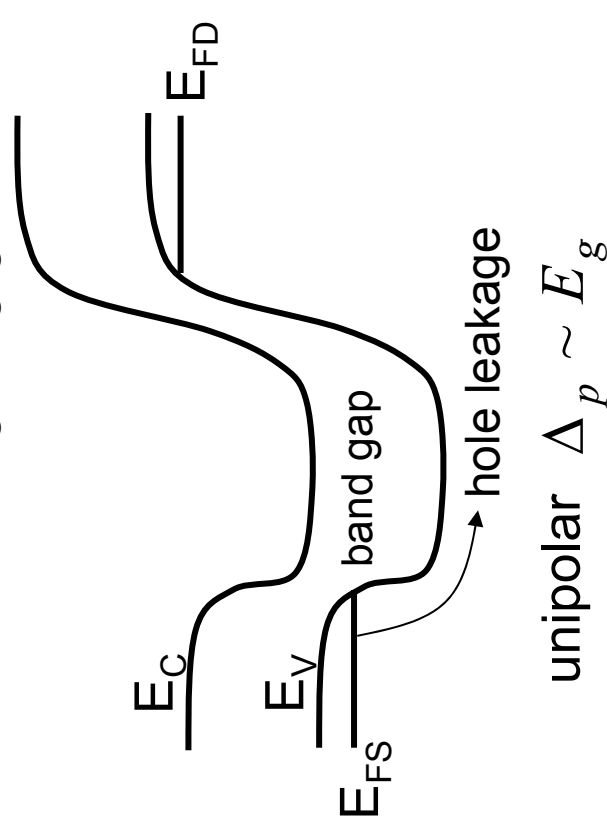
## SB CNTFET



$$\text{ambipolar } \Delta_n \sim \Delta_p \sim \frac{E_g - eV_D}{2}$$

## MOSFET-like CNTFET

electron leakage negligible

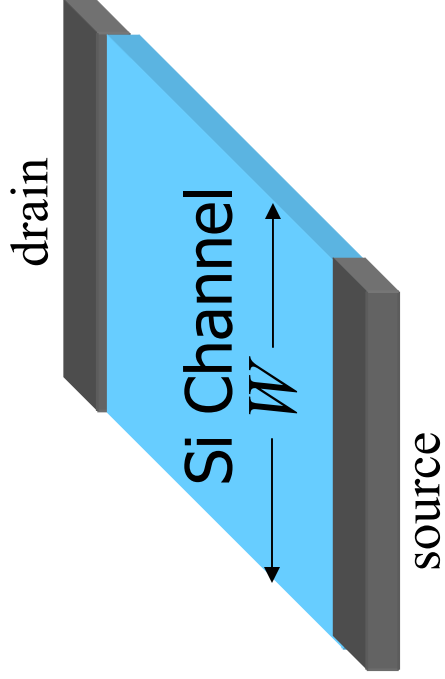


unipolar  $\Delta_p \sim E_g$

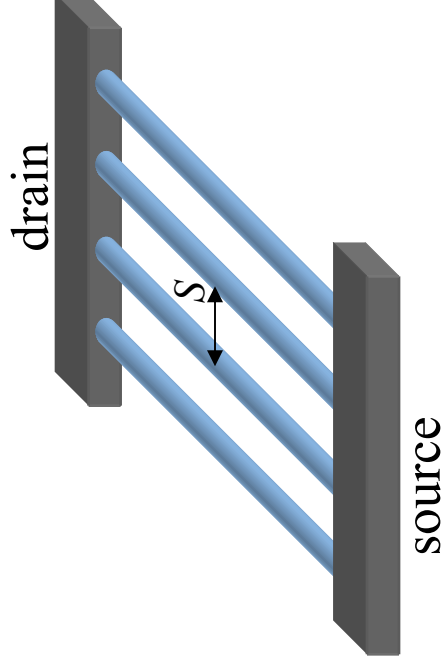
Suppressed ambipolar conduction

# How to compare to Si MOSFET?

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**Si MOSFETs**



**CNT array FETs**

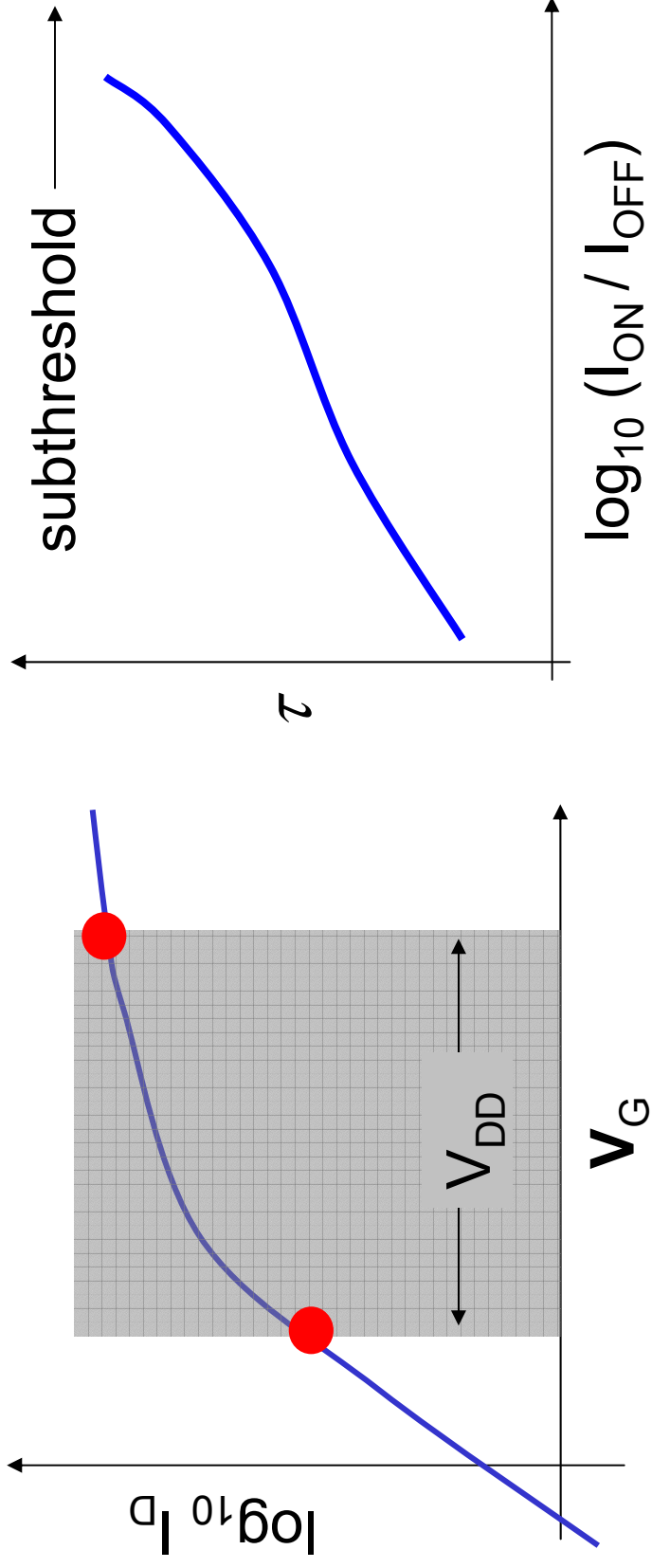
**Key device metrics:**

$$I_{ON} / I_{OFF}$$

$$\tau = C_G V_{DD} / I_{ON}$$

# $\tau$ vs. $I_{ON}/I_{OFF}$ technique

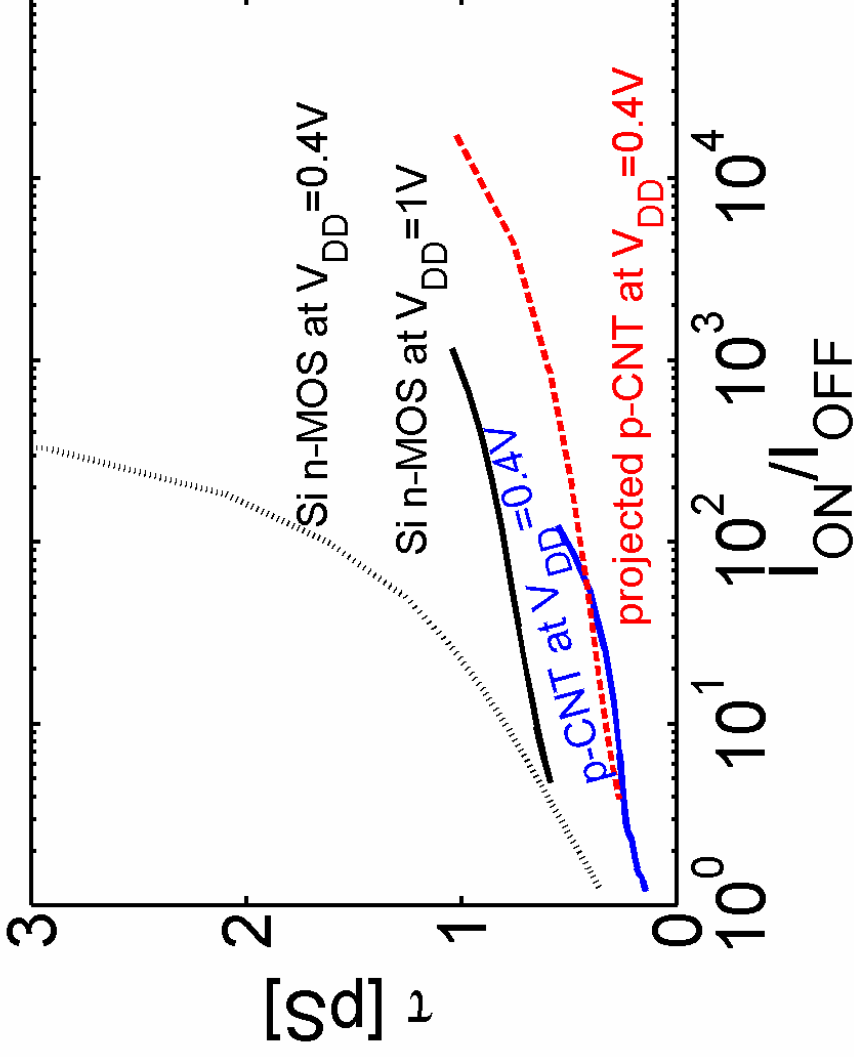
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Control of  $V_T$  shifts the window

$$\tau = C_G V_{DD} / I_{ON}$$

# Compare to 90nm Si MOSFETs



90nm Si n-MOS data from Antoniadis and Nayfeh, MIT

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# Summary: Simulation Approach

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## Quantum Transport (NEGF formalism)

- Atomistic description
- Non-equilibrium transport
- Inelastic scattering

## Three dimensional Electrostatics

- Method of moments

## Computational techniques

- recursive algorithm
- mode-space approach
- parallel simulation



# Summary

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- 1) I-V can be modeled and explained.
- 2) The CNTFET delivers near-ballistic  $I_{ON}$
- 3) Scaling  $t_{ins}$  and using high- $\kappa$  improves  $I_{ON}$
- 4) Thin  $t_{ins}$  results in ambipolar conduction
- 5) Using small  $d_{CNT}$  tube or MOSFET-like structure suppresses ambipolar conduction
- 6) The CNTFET performance is promising

# Outlook:

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## **Transistors**

- 3D electrostatics
- phonon scattering
- Advanced transistor structures
- AC characteristics

## **New devices**

- CNT optoelectronic devices
- CNT-based nanosensors